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Patenta M. Soto

Applicant: Ara H. Gharapetian
Serial No.: 10/037,208
Filed: 01/04/02
Title: SYSTEM FOR TRANSMITTING
CONTROL COMMANDS TO
ELECTRONIC DEVICES
Examiner: Not yet assigned
Group Art Unit: 2632
Client/Matter No.: 45784-00087

2632

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ORIGINALLY FILED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

AMENDMENT TRANSMITTAL

Commissioner for Patents
Washington, D.C. 20231

Sir:

- ☒ Transmitted herewith is a Preliminary Amendment in the above-identified application.
- ☐ A check in the amount of \$_____ to cover the fee for additional claims is enclosed.
- ☒ A Return Postcard.

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SEP 17 2002

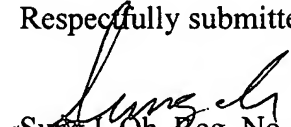
Technology Center 2000

The fee has been calculated as shown below:

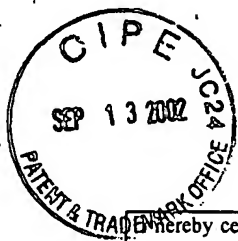
	Claims Remaining After Amendment	Highest Number Previously Paid For	Number Extra	Small Entity Rate	Add'l. Fee	Standard Rate	Add'l. Fee
TOTAL		20		x \$9		x \$18	
INDEP.		3		x \$42	\$	x \$84	
1st Presentation of Multiple Dependent Claim				x \$140	\$	x \$280	
TOTAL						TOTAL	

- ☐ Please charge my Deposit Account No. 07-1853 the amount of \$_____. A duplicate copy of this sheet is enclosed.
- ☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 07-1853.
- ☒ Any additional filing fees required under 37 C.F.R. 1.16.
- ☒ Any patent application processing fees under 37 C.F.R. 1.17.

Respectfully submitted,


Sung I. Oh, Reg. No. 45,583
Attorney for Applicant

Squire, Sanders & Dempsey, LLP
810 South Figueroa, 14th Floor
Los Angeles, CA 90017
Telephone: (213) 689-5176 * Facsimile: (213) 623-4581



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37 C.F.R. § 1.8(a) 37 C.F.R. § 1.10

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Rita M. Soto

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Attorney Docket No.: 45784-00087

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

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Technology Center 2000

Sir:

Preliminary to examination, please enter and consider the following amendment and remarks for the above patent application.

IN THE CLAIMS:

Please amend claims 1, 4, 17, 21, 26 and 32 as follows:

1. (Amended) A remote control, comprising:

a memory pre-programmed with addresses and commands for a plurality of electronic devices for a home theatre system;

a processor capable of communicating with the memory to access the addresses and commands for the plurality of electronic devices;

an initiation device capable of communicating with the processor so that when the initiation device is activated the processor encodes an address and a command into a signal for each electronic device in the plurality of electronic devices; and